

## DESCRIPTION

The MXN3382 uses advanced trench technology to provide excellent  $R_{DS(ON)}$  and low gate charge. It can be used in a wide variety of applications. It is ESD protected.

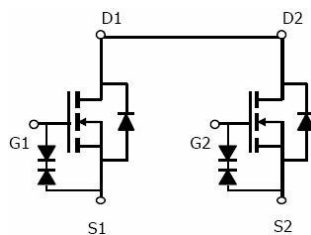
## GENERAL FEATURES

- $V_{DS}=18V$ ,  $I_D=22A$   
 $R_{DS(ON)}(Typ.)=5.0m\Omega$  @  $V_{GS}=2.5V$   
 $R_{DS(ON)}(Typ.)=4.0m\Omega$  @  $V_{GS}=3.0V$   
 $R_{DS(ON)}(Typ.)=3.6m\Omega$  @  $V_{GS}=3.8V$   
 $R_{DS(ON)}(Typ.)=3.4m\Omega$  @  $V_{GS}=4.5V$   
 ESD Rating: 4000V HBM
- High Power and current handling capability
- Lead free product is acquired
- Surface Mount Package

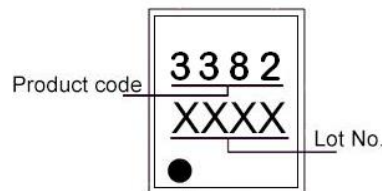
## APPLICATION

- PWM applications
- Load switch
- Power management

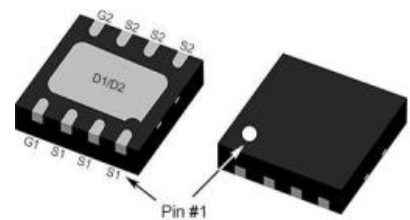
## PINOUT



Schematic diagram



Marking and pin Assignment



DFN3X3-8L top view

## ORDERING INFORMATION

Part Number	Storage Temperature	Package	Devices Per Reel
MXN3382	-55°C to 150°C	DFN3X3-8L	3000

## ABSOLUTE MAXIMUM RATINGS ( $T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	18	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current-Continuous ( $T_A=25^\circ C$ )	$I_D$	22	A
Drain Current-Continuous ( $T_A=70^\circ C$ )		16	A
Pulsed Drain Current <sup>(Note1)</sup>	$I_{DM}$	80	A
Avalanche Current	$I_{AS}$	33	A
Avalanche Energy ( $L=0.1mH$ )	$E_{AS}$	54	mJ
Maximum Power Dissipation ( $T_A=25^\circ C$ )	$P_D$	3.6	W
Maximum Power Dissipation ( $T_A=70^\circ C$ )		2.4	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^\circ C$

## THERMAL RESISTANCE

Thermal Resistance, Junction-to-Ambient <sup>(Note2)</sup>	$R_{\theta JA}$	34.7	$^\circ C/W$
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Note 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

Note 2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.



**ELECTRICAL CHARACTERISTICS** ( $T_A=25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
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**Off Characteristics**

Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	-	18	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=16V, V_{GS}=0V$	-	-	1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 10V, V_{DS}=0V$	-	-	$\pm 10$	$\mu A$

**On Characteristics** (Note 3)

Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.45	0.7	1.0	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=2.5V, I_D=6A$	3.5	5.0	6.8	$m\Omega$
		$V_{GS}=3.0V, I_D=6A$	3.2	4.0	5.0	$m\Omega$
		$V_{GS}=3.8V, I_D=7A$	3.0	3.6	4.6	$m\Omega$
		$V_{GS}=4.5V, I_D=8A$	2.8	3.4	4.3	$m\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS}=5V, I_D=6A$	25	-	-	S

**Dynamic Characteristics** (Note 4)

Input Capacitance	$C_{iss}$	$V_{DS}=10V, V_{GS}=0V, F=1.0MHz$	-	2600	-	$pF$
Output Capacitance	$C_{oss}$		-	600	-	$pF$
Reverse Transfer Capacitance (Note 4)	$C_{rss}$		-	580	-	$pF$

**Switching Characteristics**

Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=10V, I_D=1A, R_L=1\Omega, V_{GS}=4.5V, R_G=3\Omega$	-	20	-	nS
Turn-on Rise Time	$t_r$		-	40	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	72	-	nS
Turn-Off Fall Time	$t_f$		-	16	-	nS
Total Gate Charge	$Q_g$	$V_{DS}=10V, I_D=7A, V_{GS}=4.5V$	-	35	-	nC
Gate-Source Charge	$Q_{gs}$		-	5	-	nC
Gate-Drain Charge	$Q_{gd}$		-	9	-	nC

**Drain-Source Diode Characteristics**

Diode Forward Voltage (Note 3)	$V_{SD}$	$V_{GS}=0V, I_S=1A$	-	-	1.2	V
Diode Forward Current (Note 2)	$I_S$		-	-	26	A

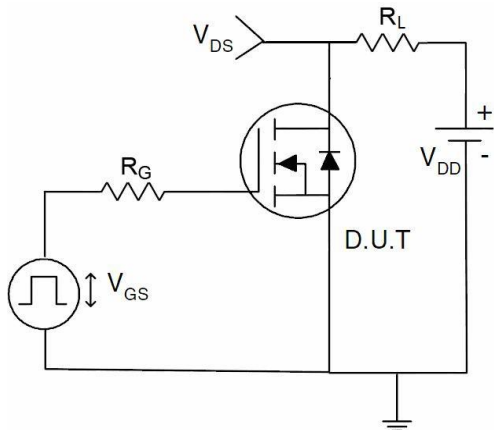
Note 2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.

Note 3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .

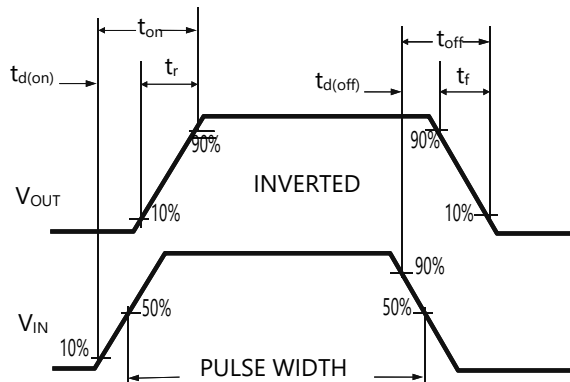
Note 4. Guaranteed by design, not subject to product.

**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

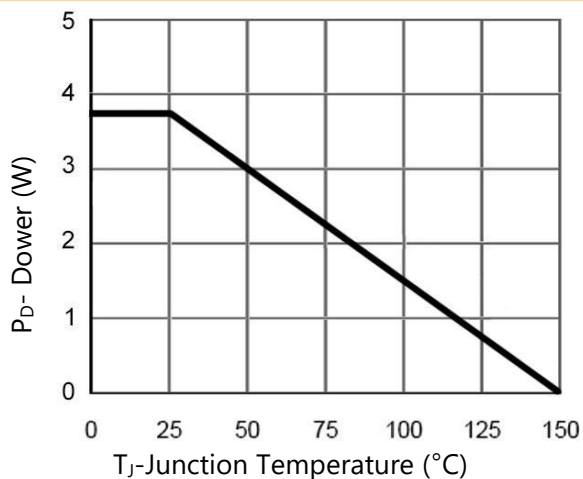
**Figure 1. Switching Test Circuit**



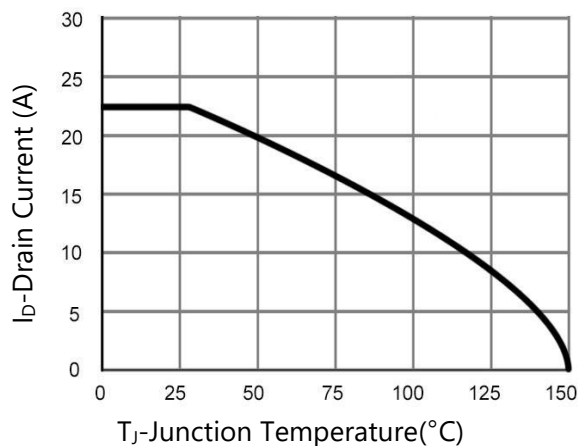
**Figure 2. Switching Waveform**



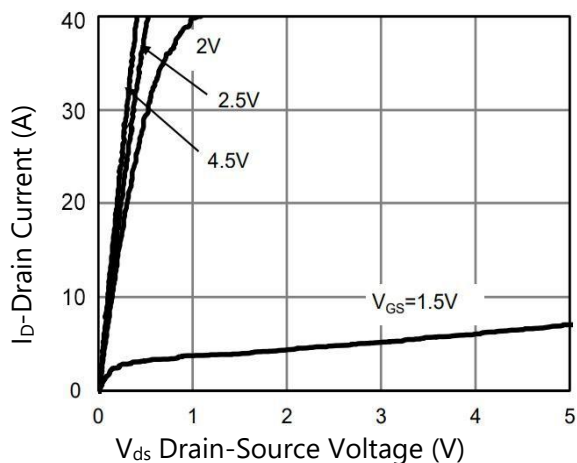
**Figure 3. Power Dissipation**



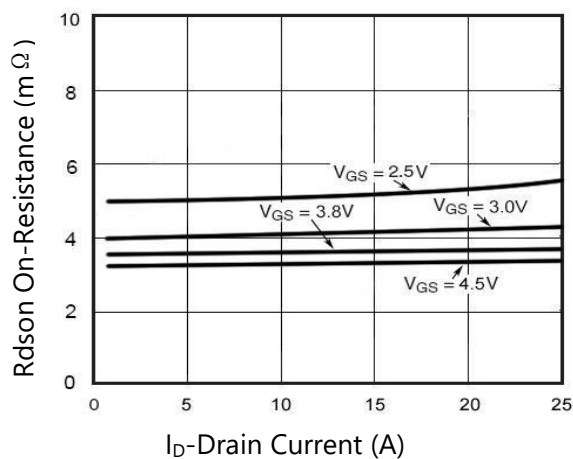
**Figure 4. Drain Current**



**Figure 5. Output Characteristics**



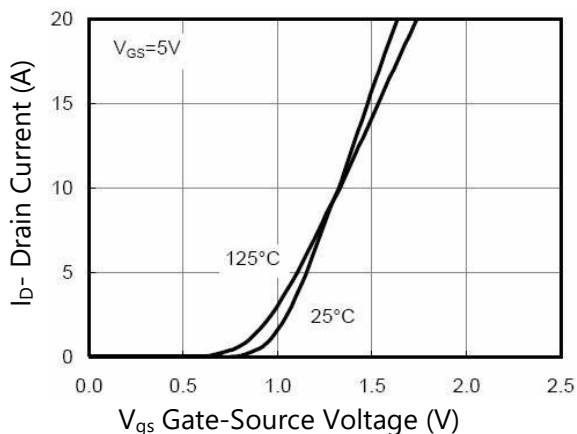
**Figure 6. R<sub>dson</sub> vs Drain Current**



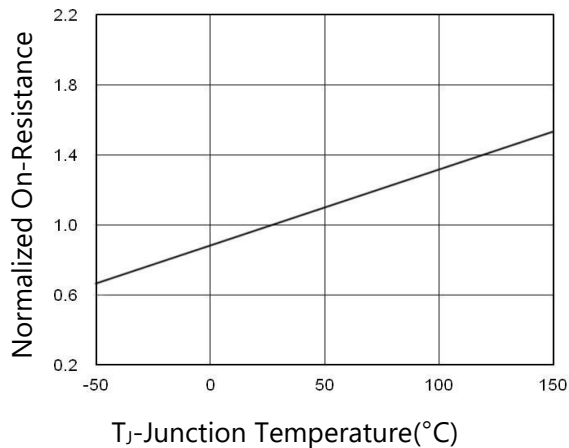


**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

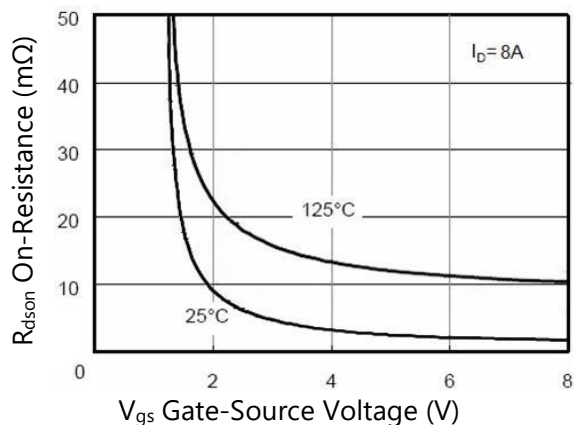
**Figure 7. Transfer Characteristics**



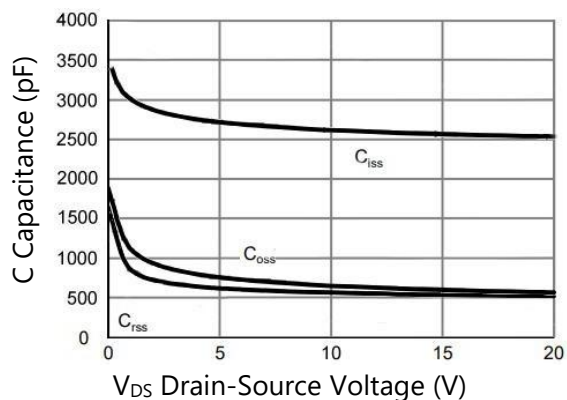
**Figure 8.  $R_{dson}$  vs Junction Temperature**



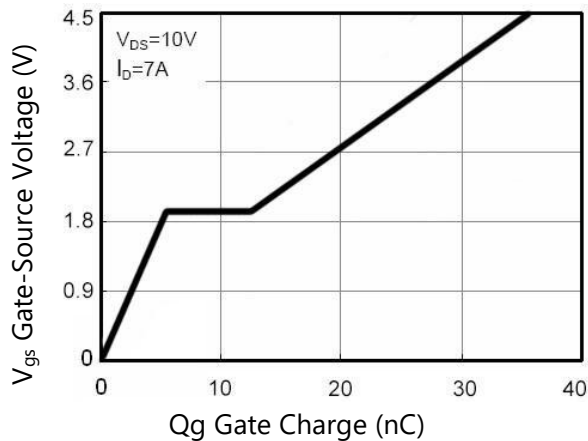
**Figure 9.  $R_{dson}$  vs  $V_{gs}$**



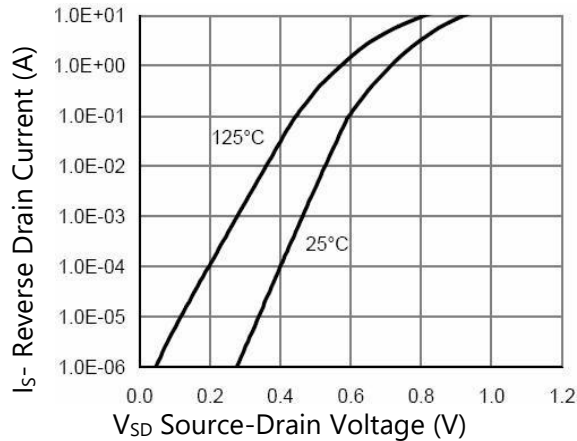
**Figure 10. Capacitance vs  $V_{DS}$**



**Figure 11. Gate Charge**

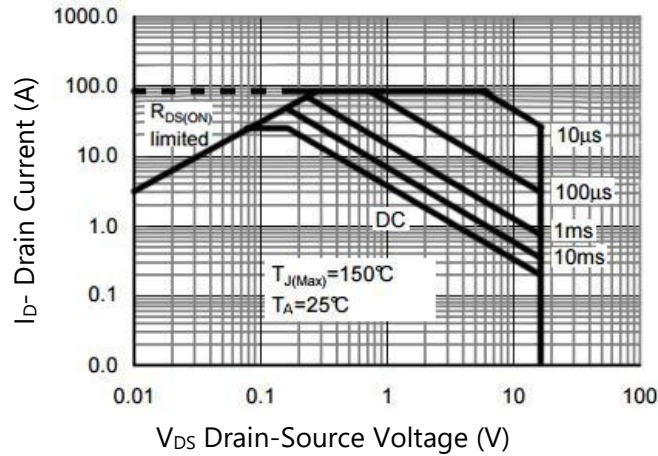


**Figure 12. Source- Drain Diode Forward**

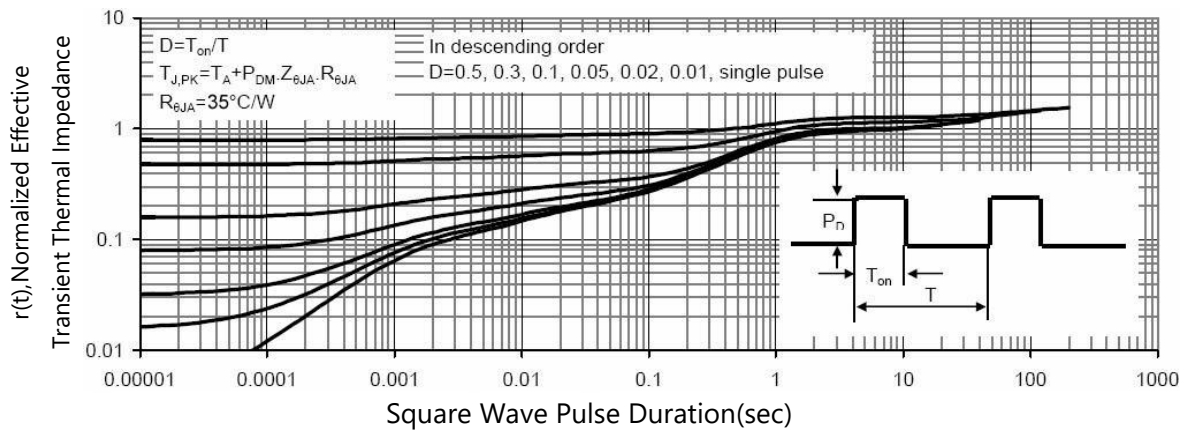


**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

**Figure 13. Safe Operation Area**

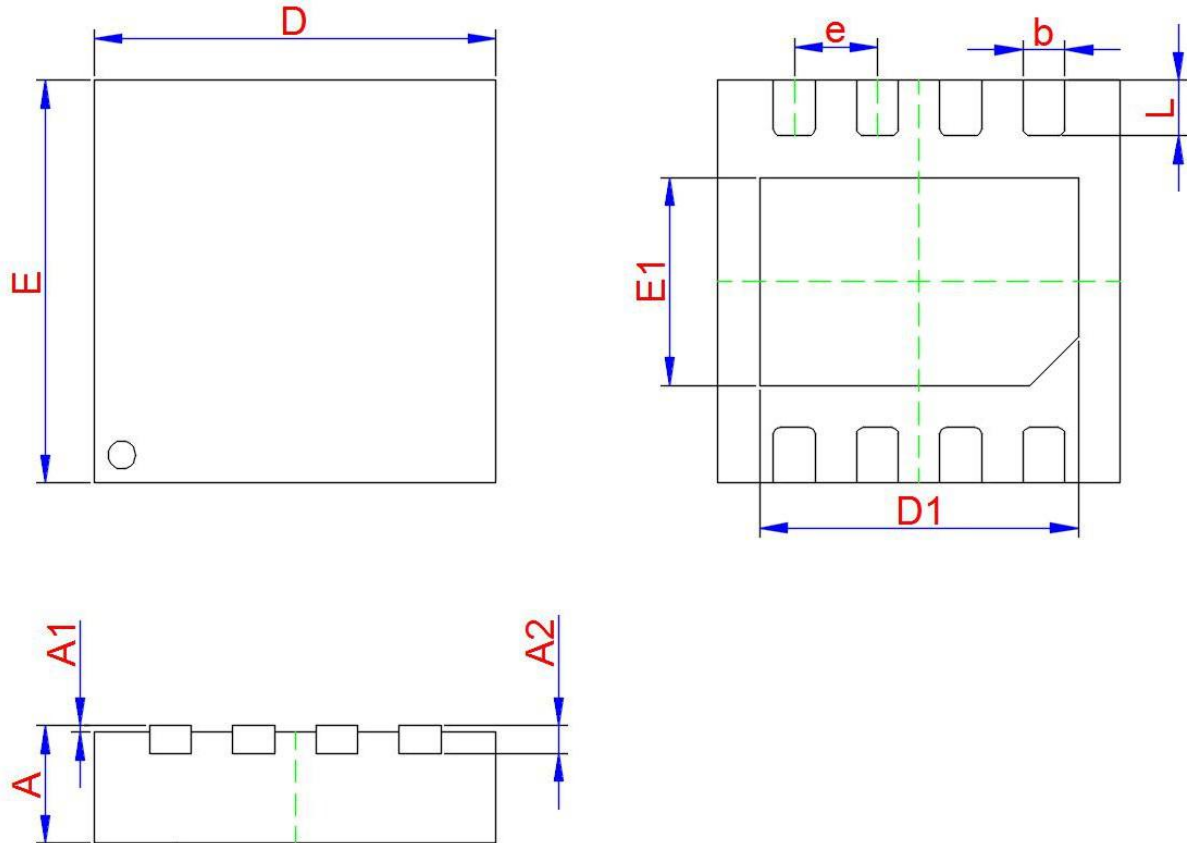


**Figure 14. Normalized Maximum Transient Thermal Impedance**



**PACKAGE INFORMATION**

DFN3X3-8L



Symbol	Dimensions In Millimeters		
	Min.	Typ.	Max.
A	0.700	0.750	0.800
A1	0.000	0.020	0.050
A2	0.203 TYP.		
b	0.250	0.300	0.350
D	2.924	3.000	3.076
D1	2.200	2.300	2.400
E	2.924	3.000	3.076
E1	1.400	1.500	1.600
e	0.650 TYP.		
L	0.350	0.400	0.450