

DESCRIPTION

The MX2801 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as -2.5V. This device is suitable for use as a load switching application and wide variety of other applications.

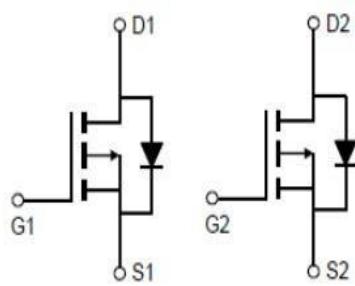
GENERAL FEATURES

- $V_{DS}=-16V$, $I_D=-7A$
 $R_{DS(ON)}(\text{Typ.})=24\text{m}\Omega$ @ $V_{GS}=-2.5V$
 $R_{DS(ON)}(\text{Typ.})=18\text{m}\Omega$ @ $V_{GS}=-4.5V$
- Advanced trench MOSFET process technology
- Ultra low on-resistance with low gate charge

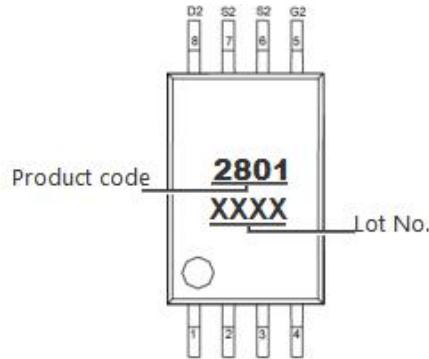
APPLICATION

- PWM applications
- Load switch
- battery charge in cellular handset

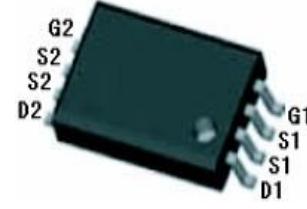
PINOUT



Schematic diagram



Pin Assignment



TSSOP-8 top view

ABSOLUTE MAXIMUM RATINGS($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-16	V
Gate-Source Voltage	V_{GS}	± 12	V
Drain Current-Continuous	I_D	-7	A
Drain Current-Pulsed ^(Note 1)	I_{DM}	-28	A
Maximum Power Dissipation	P_D	1.5	W
Operating Junction and Storage Temperature Range	T_J , T_{STG}	-55 to 150	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient ^(Note 2)	$R_{\theta JA}$	45	$^\circ\text{C}/\text{W}$
---	-----------------	----	---------------------------

Note 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

Note 2. Surface Mounted on FR4 Board, $t \leq 10$ sec.


ELECTRICAL CHARACTERISTICS($T_C=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-16	-20	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=-16\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA

On Characteristics^(Note3)

Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-0.4	-0.7	-1.0	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-2.5\text{V}, I_{\text{D}}=-6\text{A}$	-	24	30	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-7\text{A}$	-	18	23	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$V_{\text{DS}}=-5\text{V}, I_{\text{D}}=-6\text{A}$	-	33	-	S

Dynamic Characteristics^(Note4)

Input Capacitance	C_{iss}	$V_{\text{DS}}=-6\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	1370	-	pF
Output Capacitance	C_{oss}		-	350	-	pF
Reverse Transfer Capacitance	C_{rss}		-	258	-	pF

Switching Characteristics^(Note4)

Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=-6\text{V}, R_{\text{L}}=-0.75\Omega, V_{\text{GS}}=-4.5\text{V}, R_{\text{GEN}}=3\Omega$	-	11	-	nS
Turn-on Rise Time	t_{r}		-	25	-	nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	70	-	nS
Turn-Off Fall Time	t_{f}		-	42	-	nS
Total Gate Charge	Q_{g}	$V_{\text{DS}}=-6\text{V}, I_{\text{D}}=-8\text{A}, V_{\text{GS}}=-4.5\text{V}$	-	13	-	nC
Gate-Source Charge	Q_{gs}		-	2	-	nC
Gate-Drain Charge	Q_{gd}		-	3	-	nC

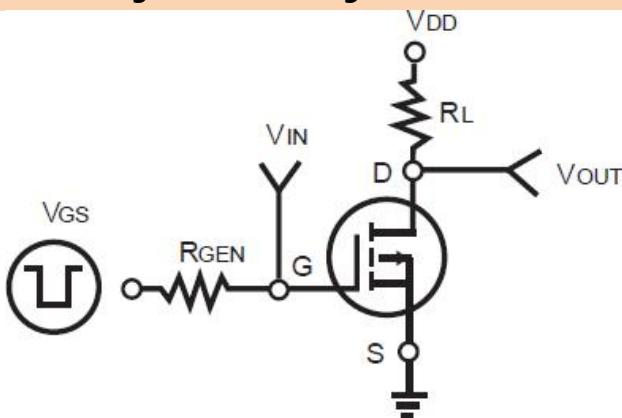
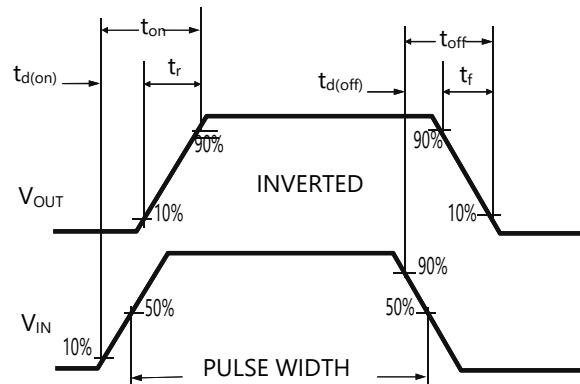
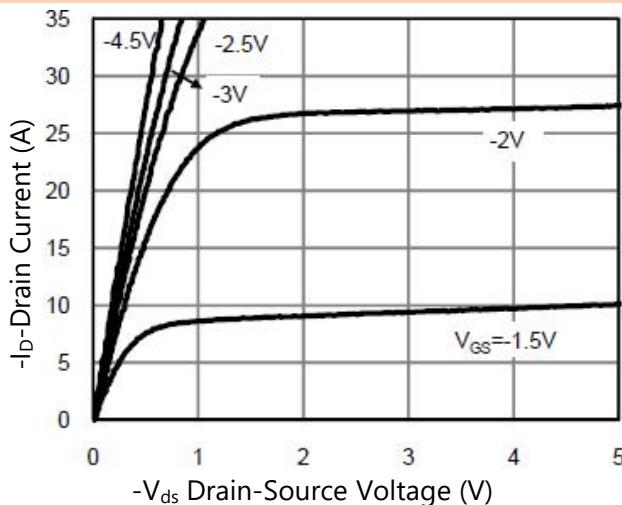
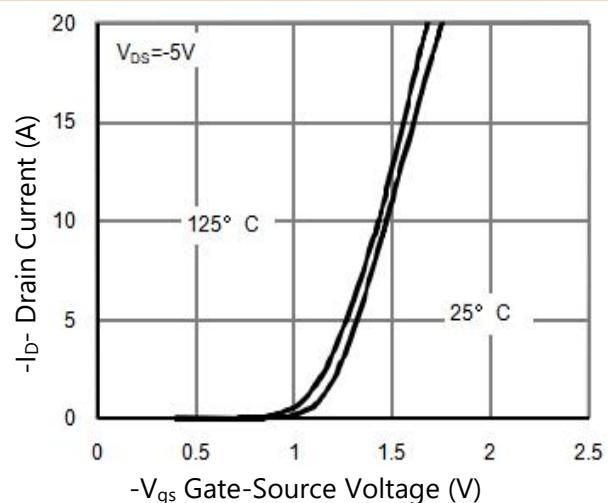
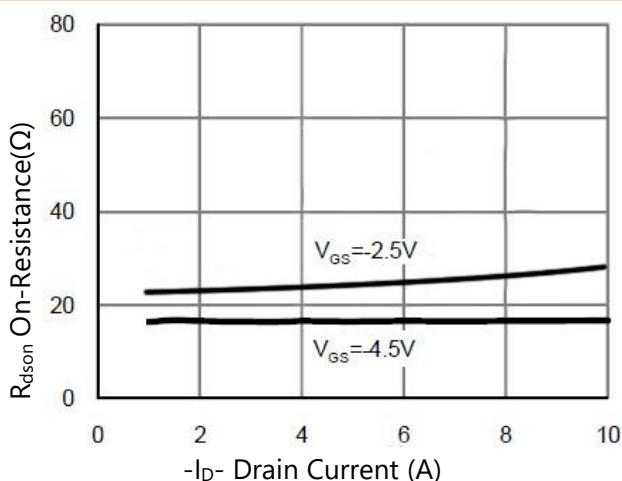
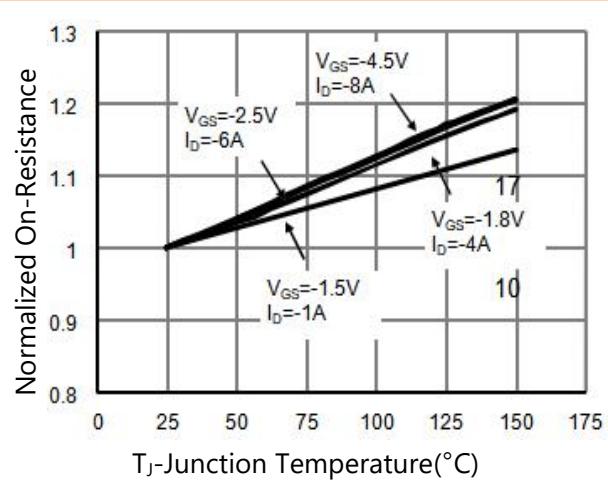
Drain-Source Diode Characteristics

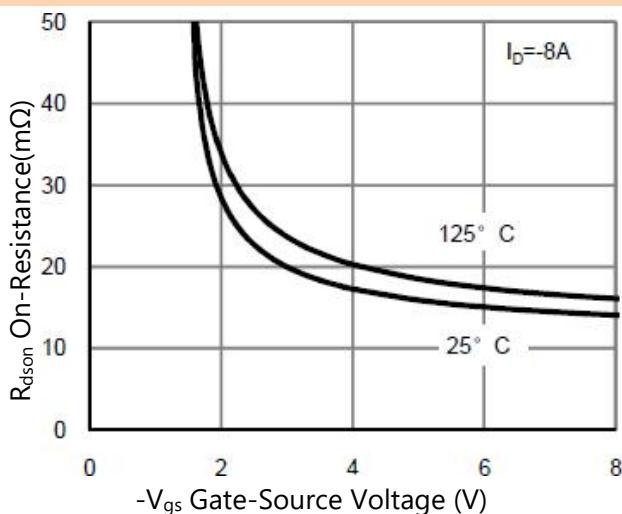
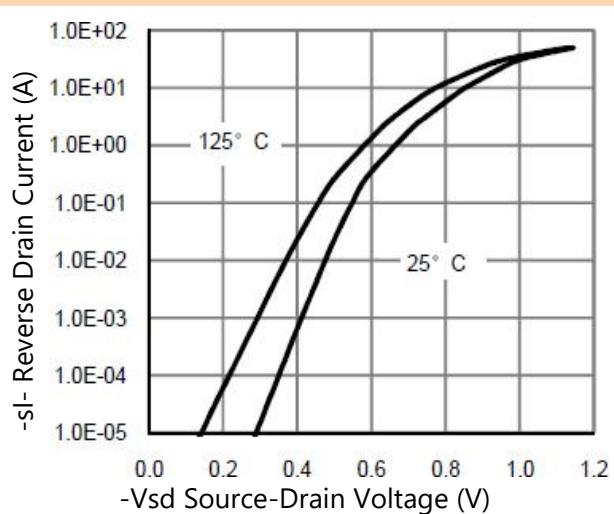
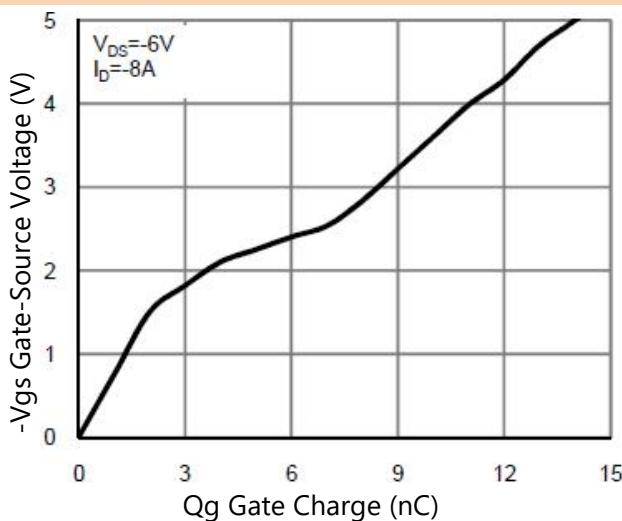
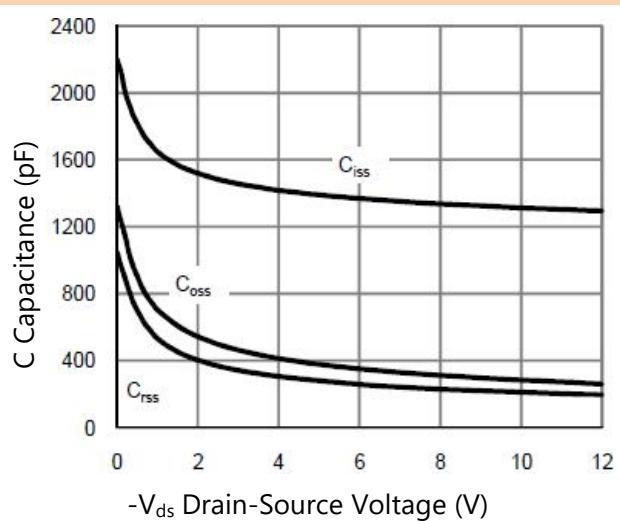
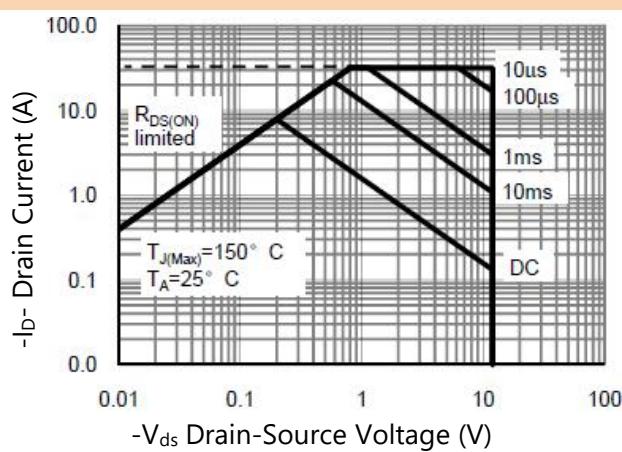
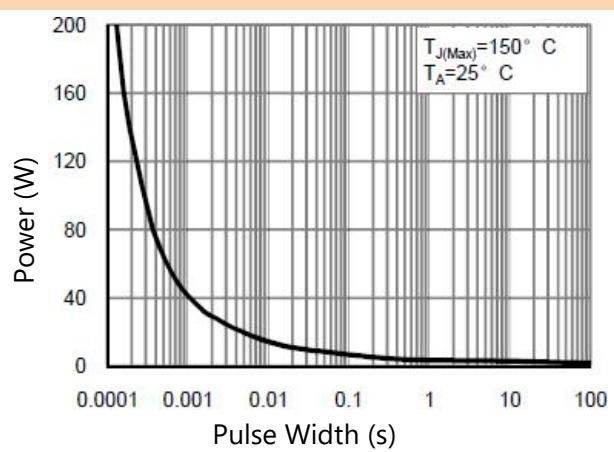
Diode Forward Voltage ^(Note3)	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=-1\text{A}$	-	-	-1.2	V
Diode Forward Current ^(Note2)	I_{S}		-	-	-3.5	A

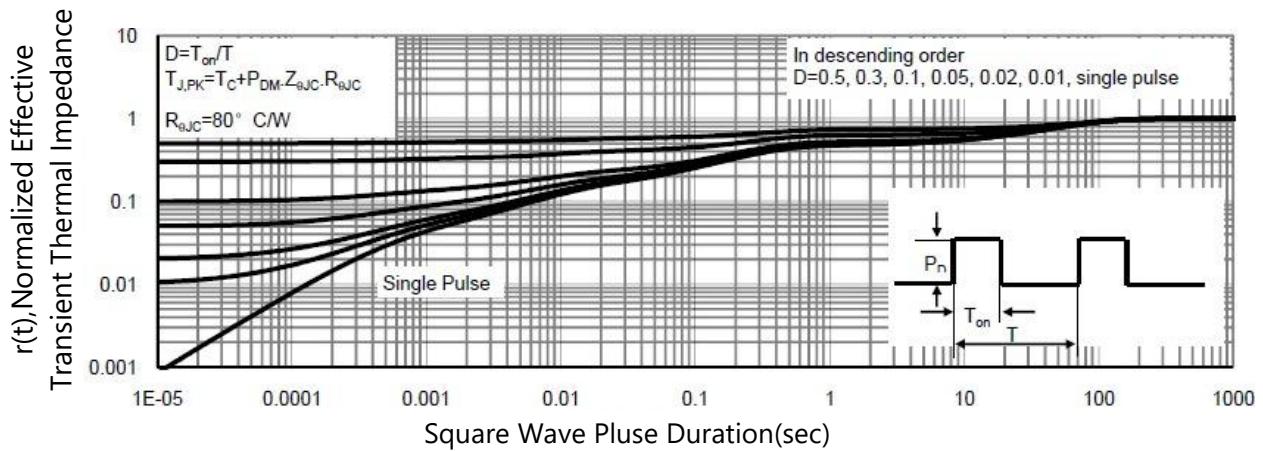
Note 2. Surface Mounted on FR4 Board, $t \leq 10$ sec.

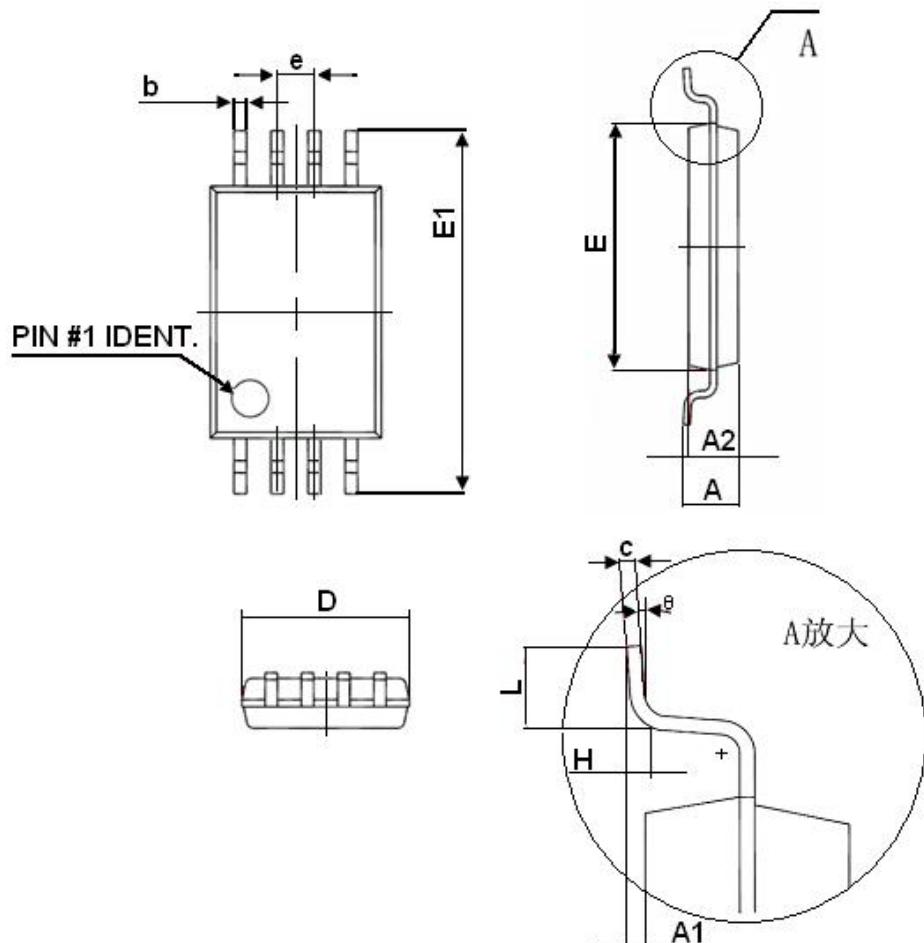
Note 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

Note 4. Guaranteed by design, not subject to production


TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS
Figure 1. Switching Test Circuit

Figure 2. Switching Waveform

Figure 3. On-Region Characteristics

Figure 4. Transfer Characteristics

Figure 5. Drain-Source On-Resistance

Figure 6. On-Resistance vs Junction Temp.



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS
Figure 7. R_{dson} vs V_{gs}

Figure 8. Source- Drain Diode Forward

Figure 9. Gate Charge

Figure 10. Capacitance vs V_{ds}

Figure 11. Safe Operation Area

Figure 12. Single Pulse Power Rating Junction-to Ambient



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS
Figure 13. Normalized Maximum Transient Thermal Impedance


PACKAGE INFORMATION
TSSOP-8


Symbol	Dimensions In Millimeters	
	Min.	Max.
D	2.900	3.100
E	4.300	4.500
b	0.190	0.300
c	0.090	0.200
E1	6.250	6.550
A	-	1.100
A2	0.800	1.000
A1	0.020	0.150
e	0.65(BSC)	
L	0.500	0.700
H	0.25(TYP)	
θ	1°	7°