

DESCRIPTION

The MX4953 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as -4.5V. This device is suitable for use as a load switch or in PWM applications.

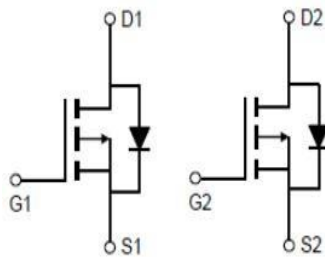
GENERAL FEATURES

- $V_{DS}=-30V$, $I_D=-5.3A$
 $R_{DS(ON)}(Typ.)=68m\Omega$ @ $V_{GS}=-4.5V$
 $R_{DS(ON)}(Typ.)=44m\Omega$ @ $V_{GS}=-10V$
- High Power and current handling capability
- Lead free product is acquired
- Surface Mount Package

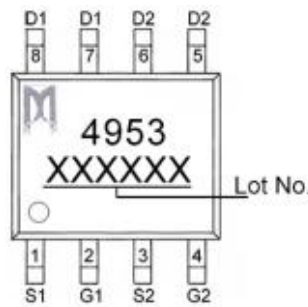
APPLICATION

- PWM applications
- Load switch
- Power management

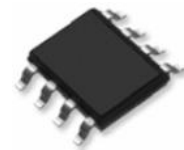
PINOUT



Schematic diagram



Pin Assignment



SOP-8 top view

ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	-5.3	A
Drain Current-Pulsed ^(Note 1)	I_{DM}	-20	A
Maximum Power Dissipation	P_D	2.6	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$

THERMAL CHARACTERISTIC

Thermal Resistance, Junction-to-Ambient ^(Note 2)	$R_{\theta JA}$	49	$^\circ C/W$
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Note 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

Note 2. Surface Mounted on FR4 Board, $t \leq 10$ sec.


ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
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Off Characteristics

Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-24V, V_{GS}=0V$	-	-	-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA

On Characteristics (Note3)

Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1	-1.6	-3	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=-4.5V, I_D=-4.2A$	-	68	90	m Ω
		$V_{GS}=-10V, I_D=-5.3A$	-	44	55	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=-15V, I_D=-4.5A$	4	7	-	S

Dynamic Characteristics (Note4)

Input Capacitance	C_{iss}	$V_{DS}=-15V, V_{GS}=0V, F=1.0MHz$	-	540	-	pF
Output Capacitance	C_{oss}		-	150	-	pF
Reverse Transfer Capacitance	C_{rss}		-	75	-	pF

Switching Characteristics (Note4)

Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-15V, I_D=-1A, V_{GS}=-10V, R_{GEN}=6\Omega$	-	8	-	nS
Turn-on Rise Time	t_r		-	14	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	18	-	nS
Turn-Off Fall Time	t_f		-	10	-	nS
Total Gate Charge	Q_g	$V_{DS}=-15V, I_D=-5.3A, V_{GS}=-10V$	-	12	-	nC
Gate-Source Charge	Q_{gs}		-	2.4	-	nC
Gate-Drain Charge	Q_{gd}		-	3.2	-	nC

Drain-Source Diode Characteristics

Diode Forward Voltage (Note3)	V_{SD}	$V_{GS}=0V, I_S=-5.3A$	-	-	-1.2	V
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 Note 3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.

Note 4. Guaranteed by design, not subject to production

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Figure 1. Switching Test Circuit

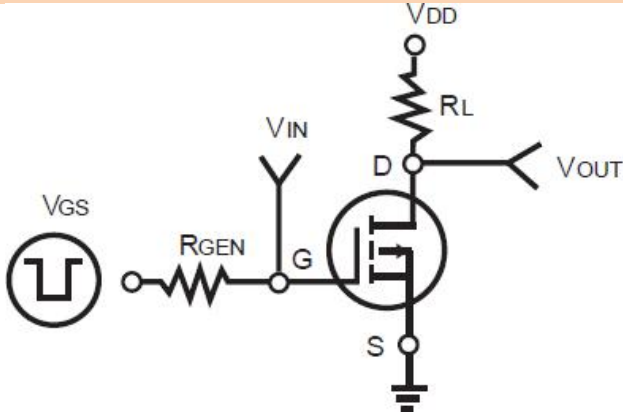


Figure 2. Switching Waveform

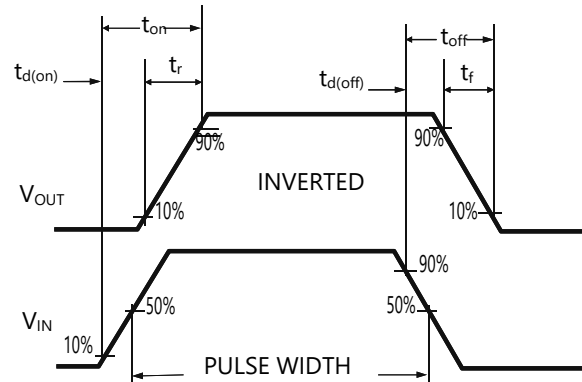


Figure 3. Power Dissipation

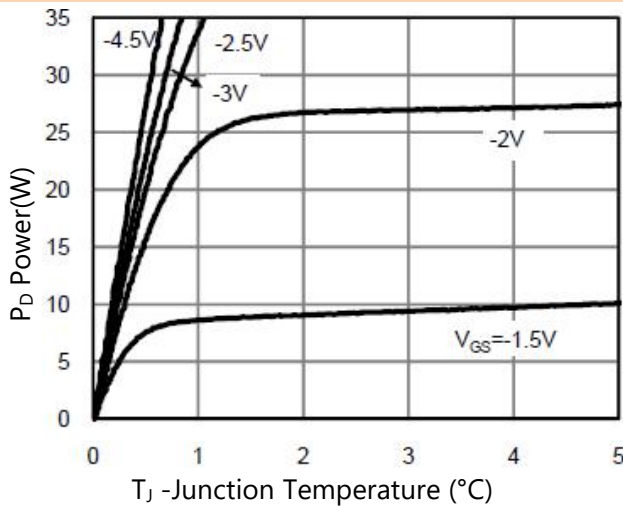


Figure 4. Drain Current

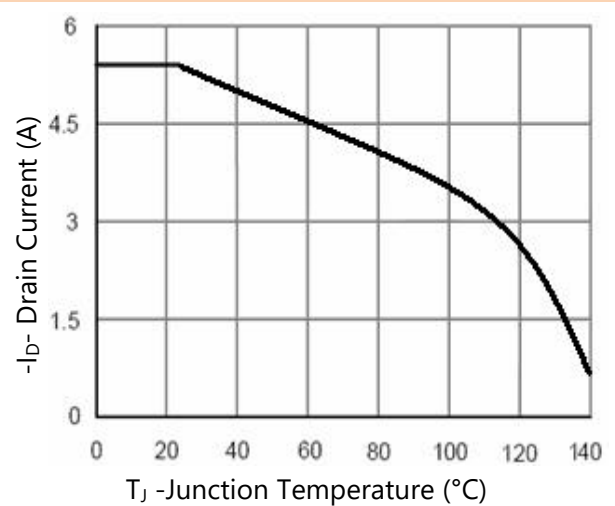


Figure 5. Output Characteristics

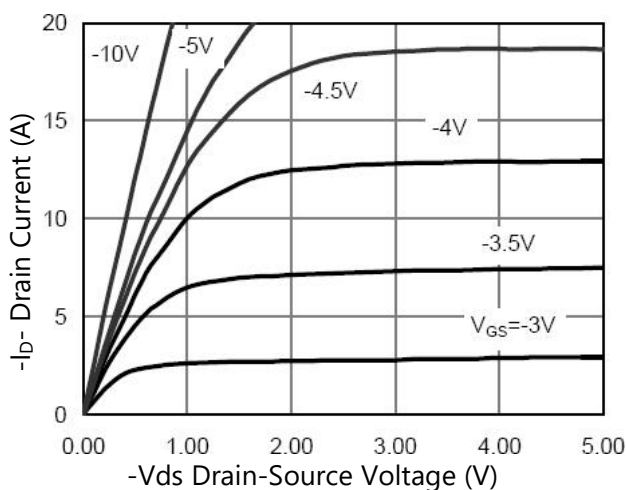
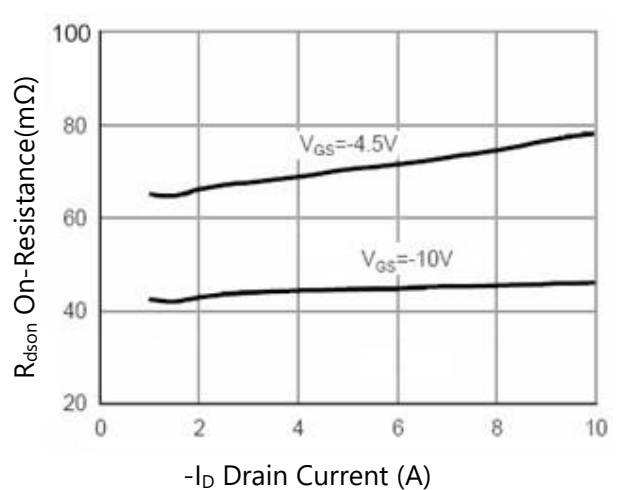


Figure 6. Drain-Source On-Resistance





TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Figure 7. Transfer Characteristics

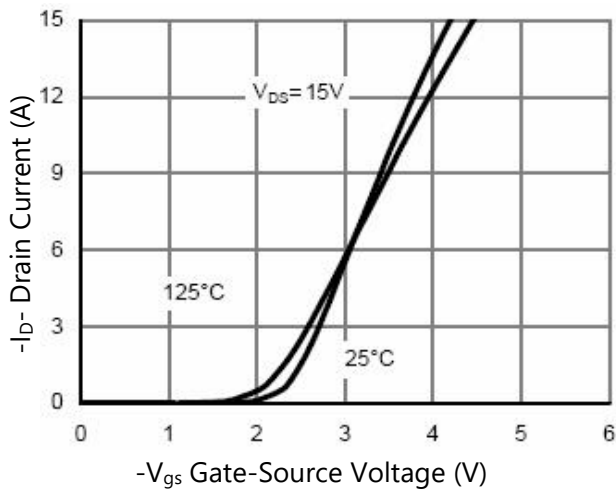


Figure 8. Source- Drain Diode Forward

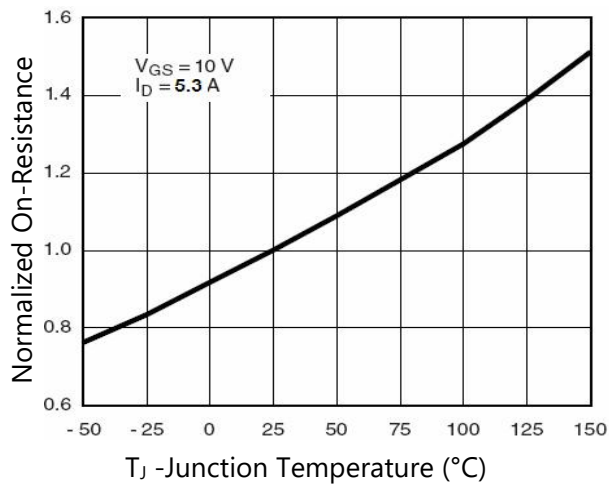


Figure 9. R_{dson} vs V_{GS}

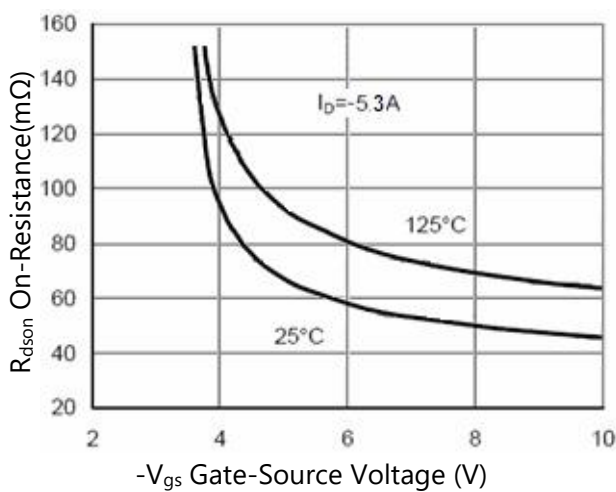


Figure 10. Capacitance vs V_{ds}

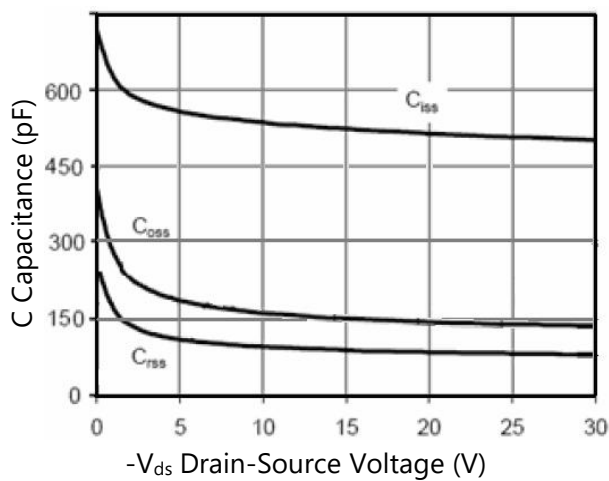


Figure 11. Gate Charge

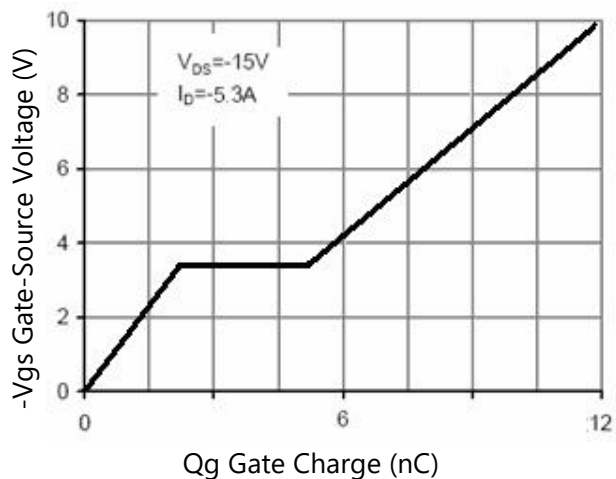
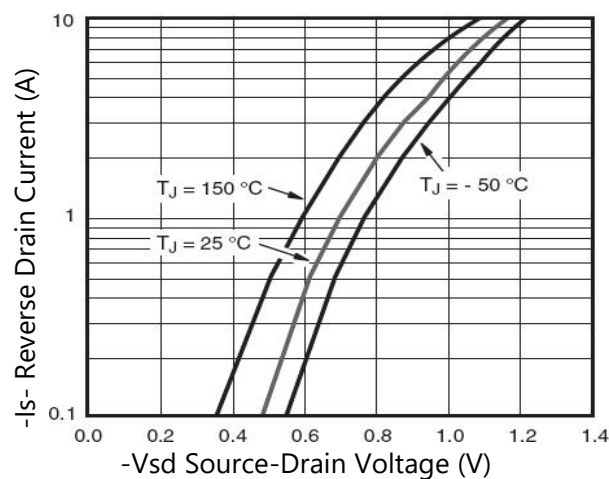


Figure 12. Source- Drain Diode Forward



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Figure 13. Safe Operation Area

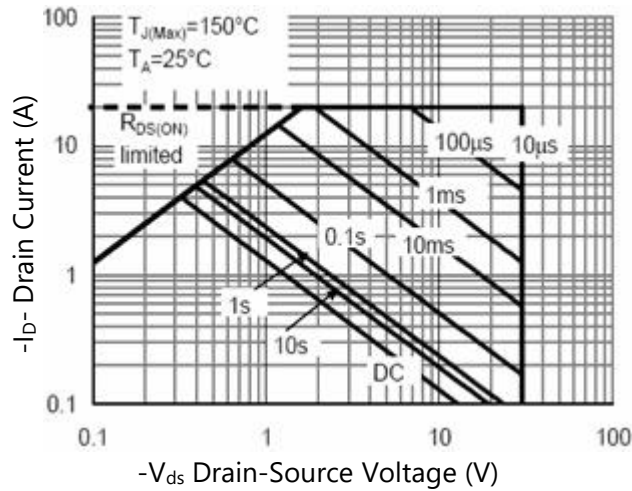
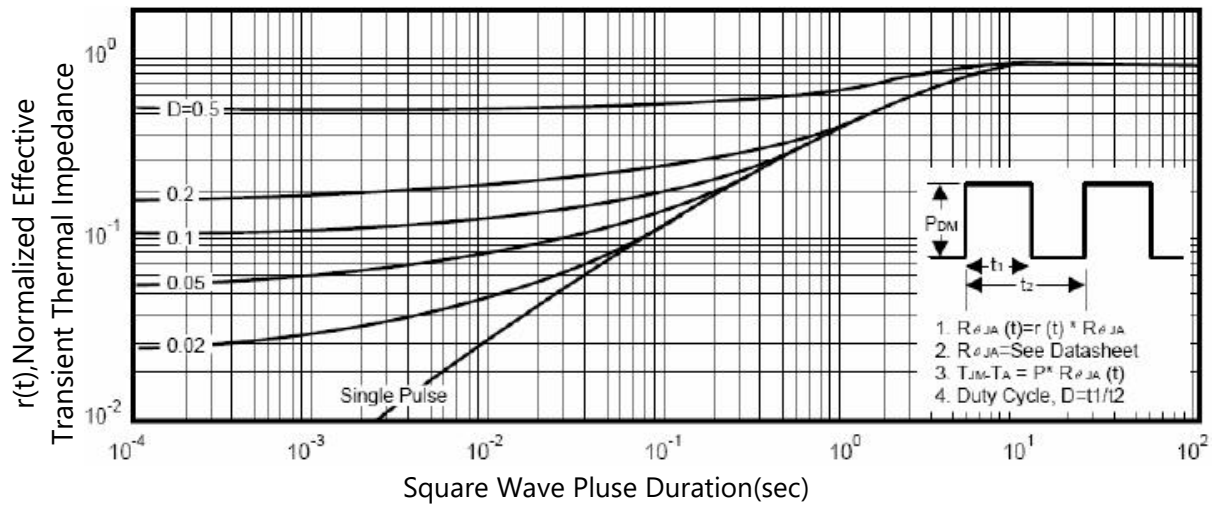
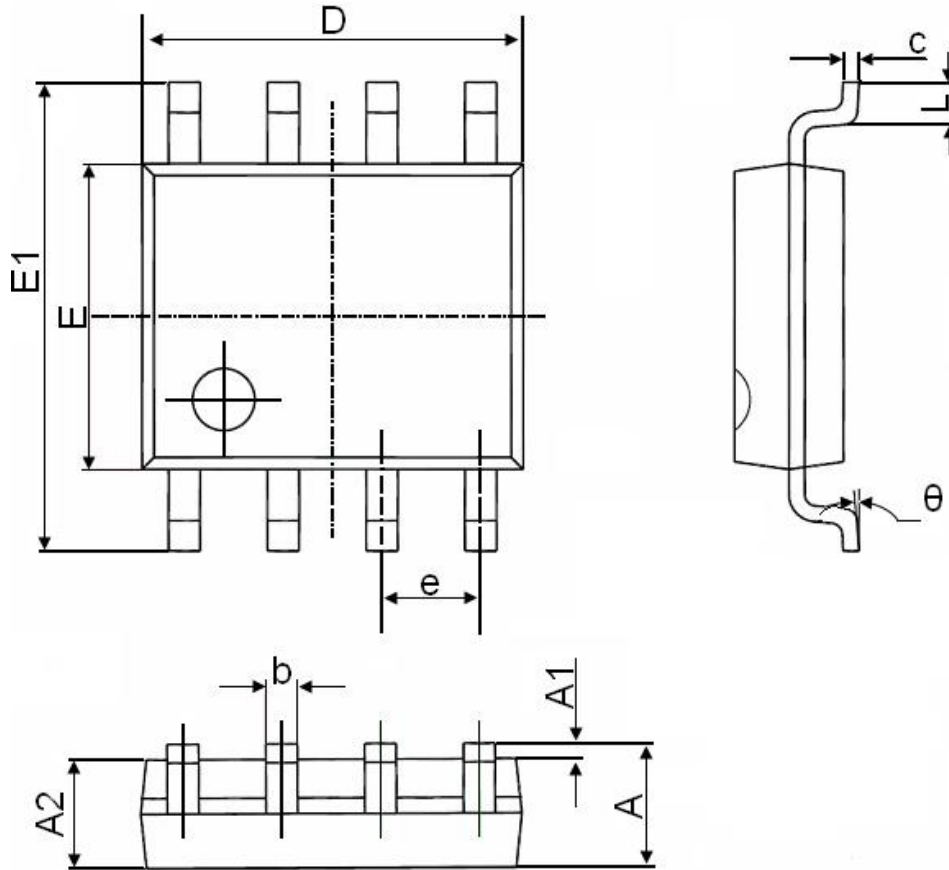


Figure 14. Normalized Maximum Transient Thermal Impedance



PACKAGE INFORMATION

SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
theta	0°	8°	0°	8°