

DESCRIPTION

The MXD065N03 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a wide variety of applications.

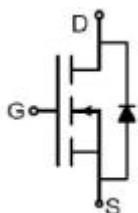
GENERAL FEATURES

- V_{DS}=30V, I_D=50A
 $R_{DS(ON)}(\text{Typ.})=7.2\text{m}\Omega$ @ V_{GS}=10V
 $R_{DS(ON)}(\text{Typ.})=11\text{m}\Omega$ @ V_{GS}=4.5V
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

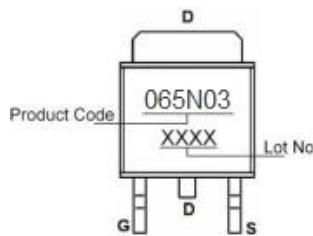
APPLICATION

- PWM applications
- Load switch
- Power management

PINOUT



Schematic diagram



Marking and Pin Assignment



TO-252 top view

ORDERING INFORMATION

Device	Storage Temperature	Package	Devices Per Reel
MXD065N03	-55°C to 175°C	TO-252	2500

KEY PERFORMANCE PARAMETERS ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage(V _{GS} =0V)	V _{DS}	30	V
Gate-Source Voltage(V _{DS} =0V)	V _{GS}	± 20	V
Drain Current-Continuous($T_C=25^\circ\text{C}$) ^(Note1)	I _D	50	A
Drain Current-Continuous($T_C=100^\circ\text{C}$)	I _D	35	A
Drain Current-Continuous@Current-Pulsed ^(Note2)	I _{DM(pluse)}	200	A
Maximum Power Dissipation($T_C=25^\circ\text{C}$)	P _D	50	W
Maximum Power Dissipation($T_C=100^\circ\text{C}$)	P _D	25	W
Single Pulse Avalanche Energy ^(Note3)	E _{AS}	90	mJ
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 to 175	°C

THERMAL CHARACTERISTIC

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	3	°C/W

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

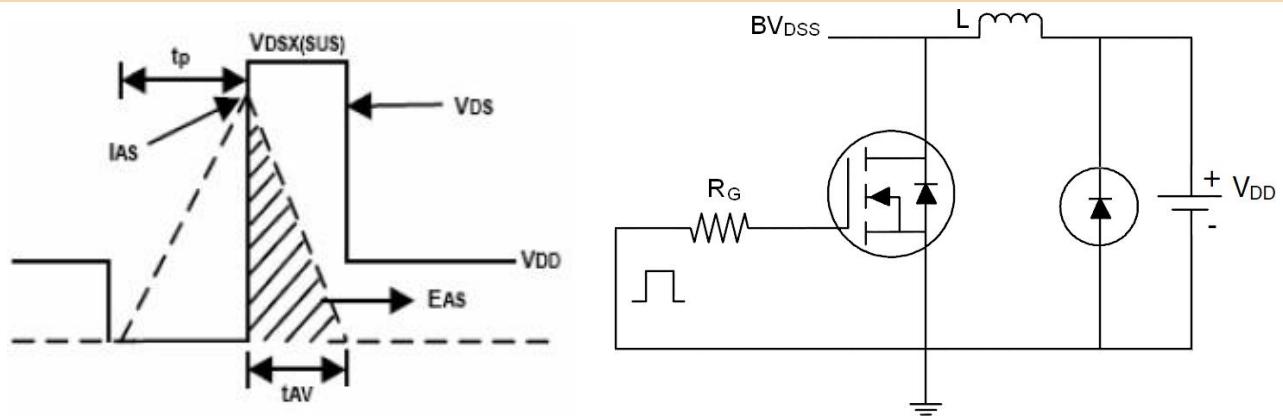
Notes 3.E_{AS} condition: T_J=25°C, V_{DD}=30V, V_G=10V, R_G=25Ω,


ELECTRICAL CHARACTERISTICS($T_A=25^\circ\text{C}$ unless otherwise noted)

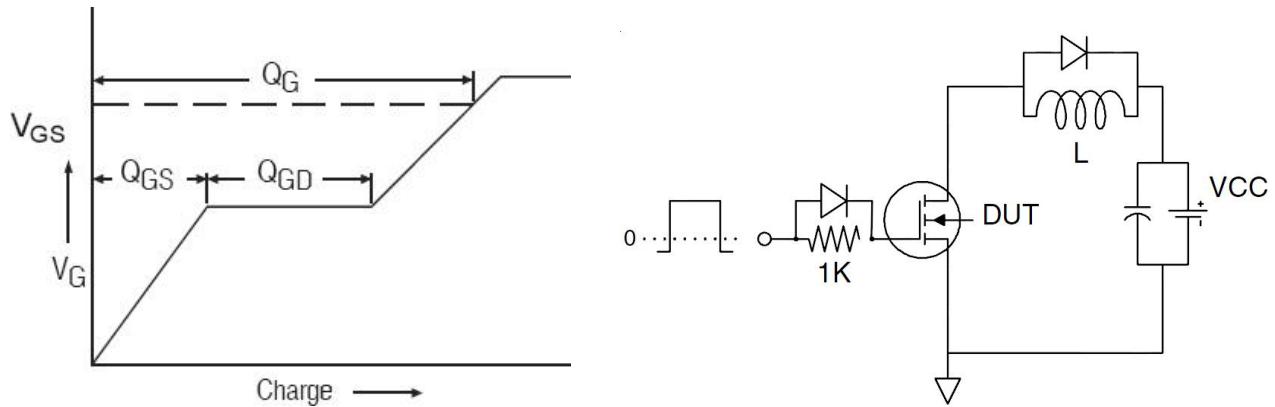
Parameter	Symbol	Condition	Min	Typ	Max	Unit
On/Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.0	1.5	2.5	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=20\text{A}$	-	7.2	9	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=15\text{A}$	-	11	17	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$V_{\text{DS}}=4.5\text{V}, I_{\text{D}}=15\text{A}$	10	20	-	S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	1050	-	pF
Output Capacitance	C_{oss}		-	145	-	pF
Reverse Transfer Capacitance	C_{rss}		-	120	-	pF
Gate resistance	R_g	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	2	-	Ω
Total Gate Charge	Q_g	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=12\text{A}, V_{\text{GS}}=25\text{V}$	-	22	-	nC
Gate-Source Charge	Q_{gs}		-	4	-	nC
Gate-Drain Charge	Q_{gd}		-	7	-	nC
Switching Characteristics						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DS}}=15\text{V}, R_L=0.75\Omega$ $V_{\text{GS}}=10\text{V}, R_{\text{GEN}}=3\Omega$	-	7	-	nS
Turn-on Rise Time	t_r		-	22	-	nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	30	-	nS
Turn-Off Fall Time	t_f		-	5	-	nS
Source-Drain Diode Characteristics						
Source-Drain Current(Body Diode)	I_{SD}		-	-	50	A
Forward On Voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{SD}}=20\text{A}$	-	-	1.2	V

TEST CIRCUIT

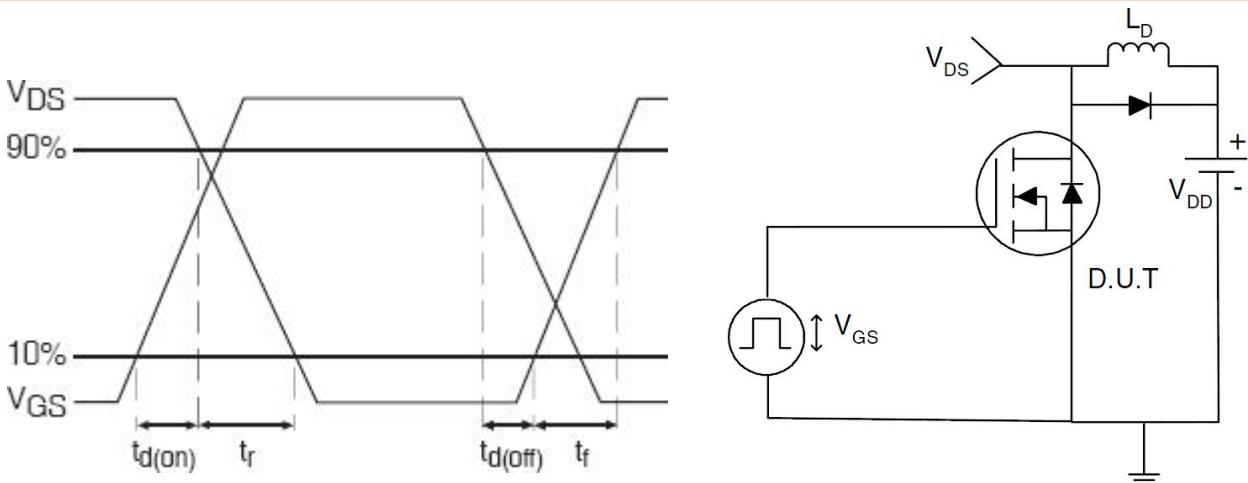
1) EAS Test Circuits



2) Gate Charge Test Circuit



3) Switch Time Test Circuit



TYPICAL PERFORMANCE CHARACTERISTICS

Figure1. Output Characteristics

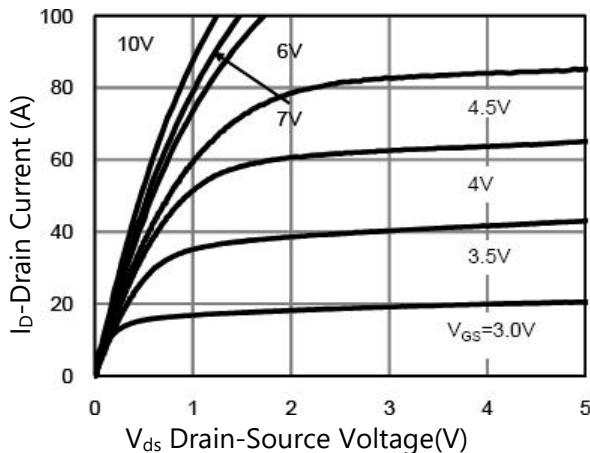


Figure2. Transfer Characteristics

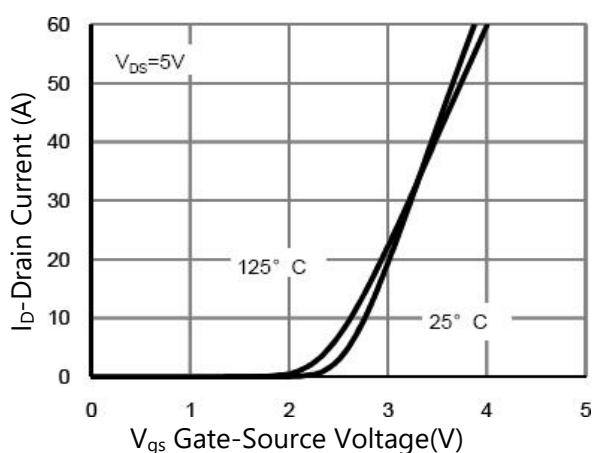


Figure3. BV_{DSS} vs Junction Temperature

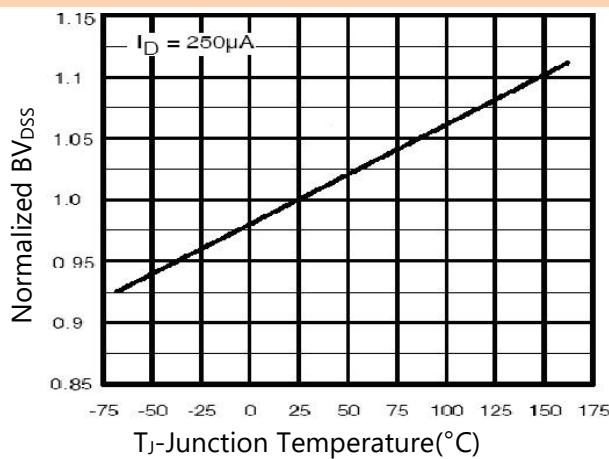


Figure4. Drain Current

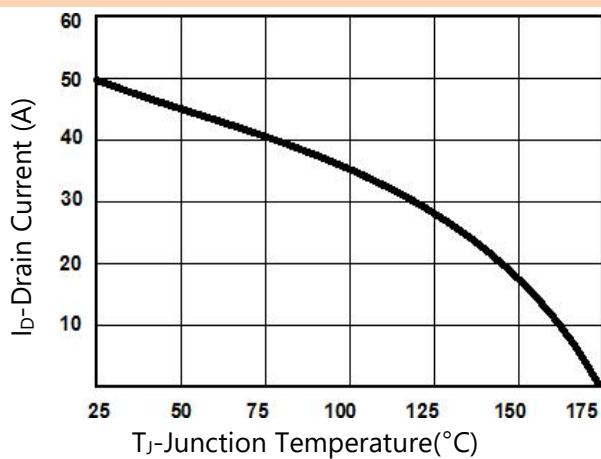


Figure5. $V_{GS(th)}$ vs Junction Temperature

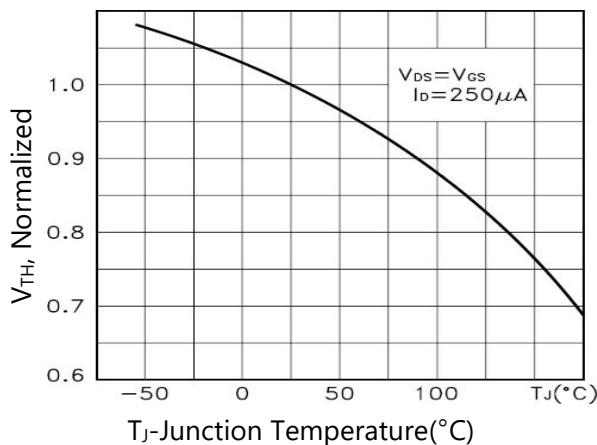
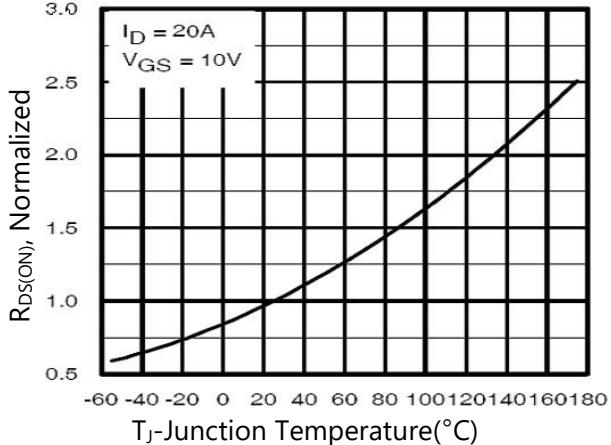


Figure6. $R_{DS(ON)}$ vs Junction Temperature



TYPICAL PERFORMANCE CHARACTERISTICS

Figure7. Gate Charge Waveforms

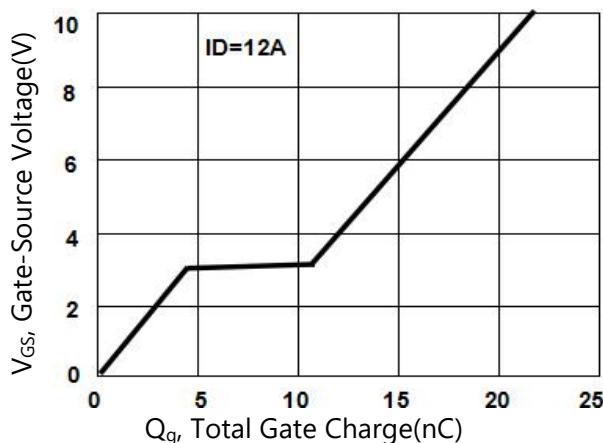


Figure8. Capacitance

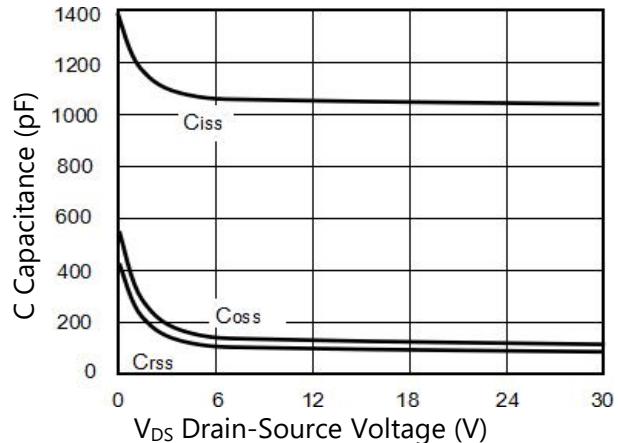


Figure9. Body Diode Forward

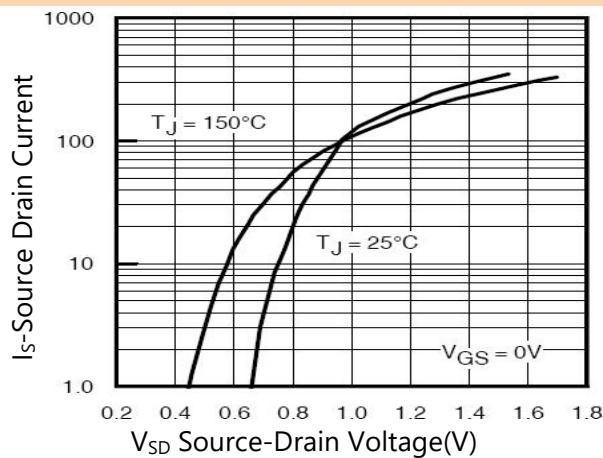


Figure10. Maximum Safe Operating Area

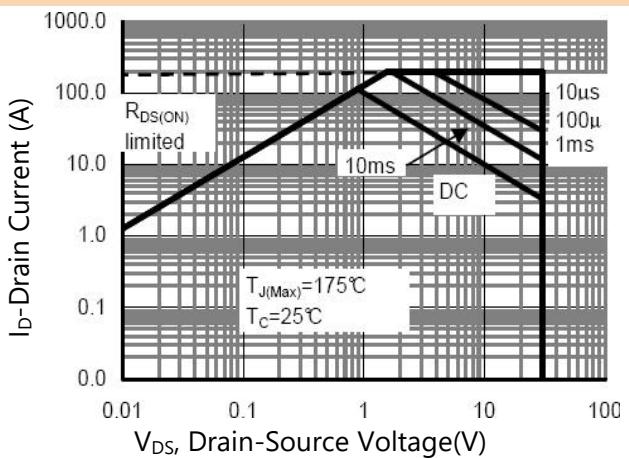
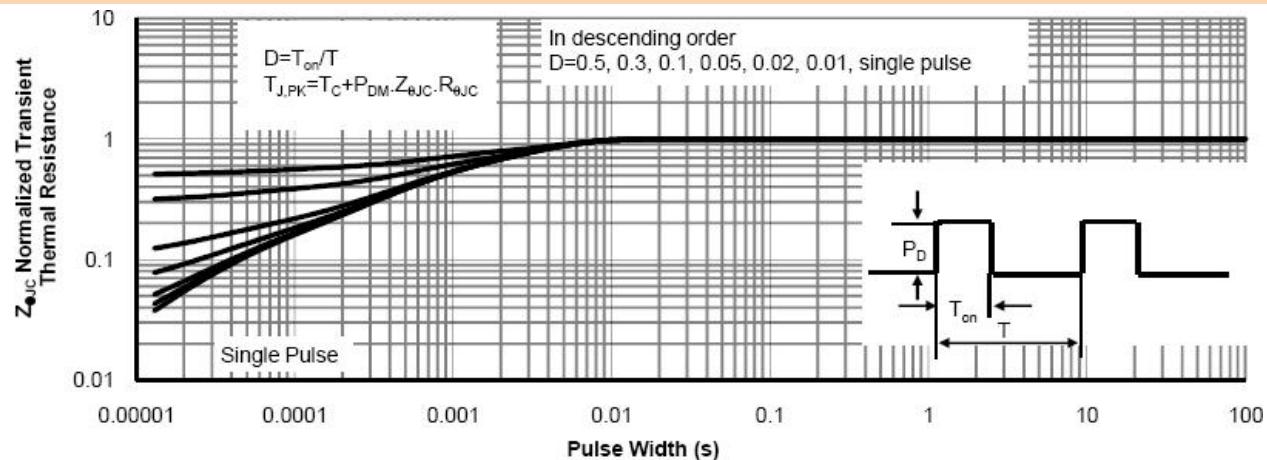
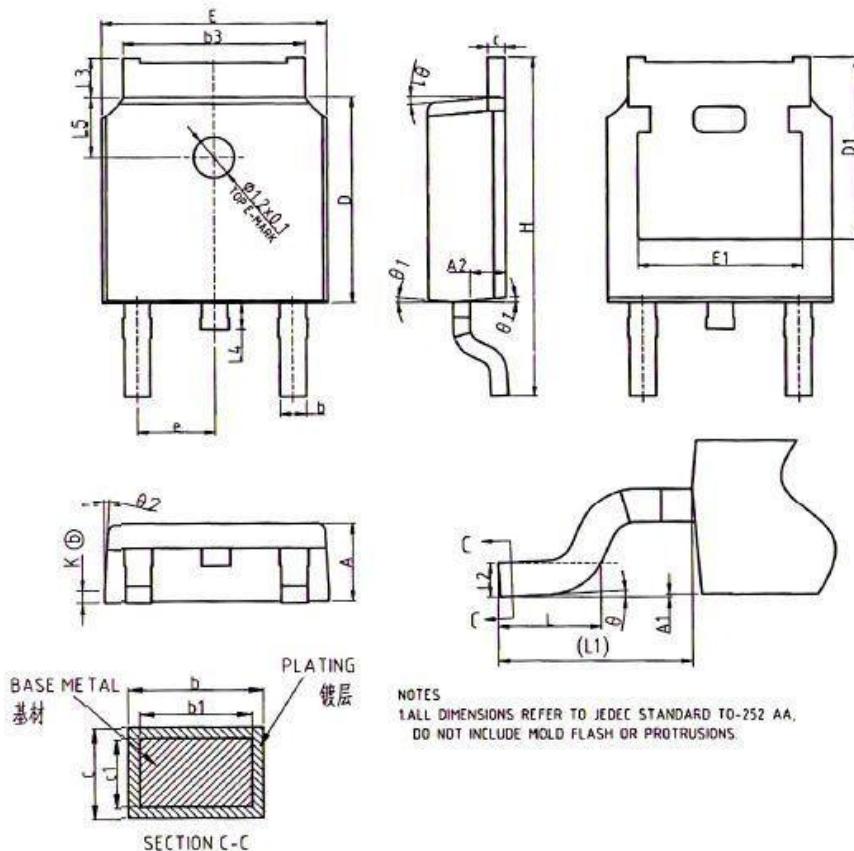


Figure11. Normalized Maximum Transient Thermal Impedance



PACKAGE INFORMATION

TO-252


SYMBOL	COMMON DIMENSIONS IN MILLIMETERS			SYMBOL	COMMON DIMENSIONS IN MILLIMETERS		
	MIN	NOM	MAX		MIN	NOM	MAX
A	2.20	2.30	2.38	H	9.90	10.10	10.30
A1	0.00	-	0.10	L	1.40	1.50	1.70
A2	0.97	1.07	1.17	L1	2.90REF		
b	0.72	0.78	0.85	L2	0.51BSC		
b1	0.71	0.76	0.81	L3	0.90	-	1.25
b3	5.23	5.33	5.46	L4	0.60	0.80	1.00
c	0.47	0.53	0.58	L5	1.70	1.80	1.90
c1	0.46	0.51	0.56	θ	0°	-	8°
D	6.00	6.10	6.20	θ1	5°	7°	9°
D1	5.30REF			θ2	5°	7°	9°
E	6.50	6.60	6.70	K	0.40REF		
E1	4.70	4.83	4.92				
e	2.286BSC						