DESCRIPTION

The MXN0406G uses advanced trench technology to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

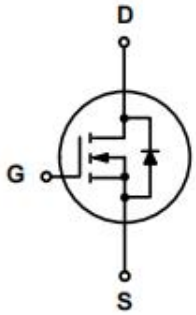
GENERAL FEATURES

- V_{DS}=60V, I_D=78A
  $R_{DS(ON)}$(Typ.)=4.2mΩ @ V_{GS}=10V
  $R_{DS(ON)}$(Typ.)=4.9mΩ @ V_{GS}=6V
  $R_{DS(ON)}$(Typ.)=5.9mΩ @ V_{GS}=4.5V
- Low gate charge
- Super-mounted package

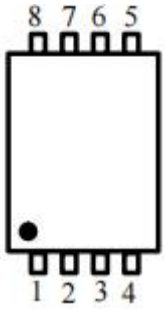
APPLICATION

- Motor driver appliances
- High power inverter system
- Adapter appliances

PINOUT



Schematic diagram



PDFN5X6-8L top view

Pin	Description
1,2,3	Source
4	Gate
5,6,7,8	Drain

ORDERING INFORMATION

Part Number	StorageTemperature	Package	Devices Per Reel
MXN0406G	-55°C to 150°C	PDFN5X6	5000

ABSOLUTE MAXIMUM RATINGS(T_C=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current(V_{GS}=10V)(Note1)	I_D	78	A
Drain Current(T_C=100°C, V_{GS}=10V)(Note1)	I_D	49	A
Pulsed Drain Current(V_{GS}=10V)(Note1)(Note2)	I_{DM}	312	A
Diode Forward Current	I_S	78	A
Total Power Dissipation(Note1)	P_{tot}	56	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C
Single Pulsed Avalanche Energy(Note1)	E_{AS}	162	mJ
Thermal Resistance, Junction-to-Ambient(Note1)	$R_{\theta JA}$	43	°C/W
Thermal Resistance, Junction-to-Ambient(Note1)	$R_{\theta JC}$	2.2	°C/W

Note 1. Surface Mounted on 1 in^2 pad area, t ≤ 10 sec
Note 2. Pulse width ≤ 300μs, duty cycle ≤ 2%

ELECTRICAL CHARACTERISTICS(T_A=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=250μA	60	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=48V, V_{GS}=0V	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
On Characteristics						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250μA	1.0	-	2.0	V
Drain-Source On-State Resistance(Note1)	$R_{DS(ON)}$	V_{GS}=10V, I_D=20A	-	4.2	4.8	mΩ
		V_{GS}=6V, I_D=15A	-	4.9	6.0	mΩ
		V_{GS}=4.5V, I_D=10A		5.9	7.5	mΩ
Dynamic Characteristics(Note2)						
Input Capacitance	C_{iss}	V_{DS}=30V, V_{GS}=0V, F=1.0MHz	-	1855	-	pF
Output Capacitance	C_{oss}		-	736	-	pF
Reverse Transfer Capacitance	C_{rss}		-	56	-	pF
Switching Characteristics(Note2)						
Turn-on Delay Time	$t_{d(on)}$	V_{DS}=30V, I_D=20A, V_{GEN}=10V, R_G=3.9Ω, R_L=1.5Ω,	-	9	-	nS
Turn-on Rise Time	t_r		-	26	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	34	-	nS
Turn-Off Fall Time	t_f		-	29	-	nS
Total Gate Charge	Q_g	V_{DS}=30V, I_D=20A, V_{GS}=10V	-	41	-	nC
Gate-Source Charge	Q_{gs}		-	7.6	-	nC
Gate-Drain Charge	Q_{gd}		-	9	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage(Note1)	V_{SD}	V_{GS}=0V, I_{SD}=20A	-	-	1.3	V
Reverse Recovery Time	t_{rr}	I_{SD}=20A, dI_{SD}/dt=100A/μs	-	36	-	nS
Reverse Recovery Charge	Q_{rr}		-	19	-	nC

Note 1. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
Note 2. Guaranteed by design, not subject to prduction testing

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Figure 1. Power Capability

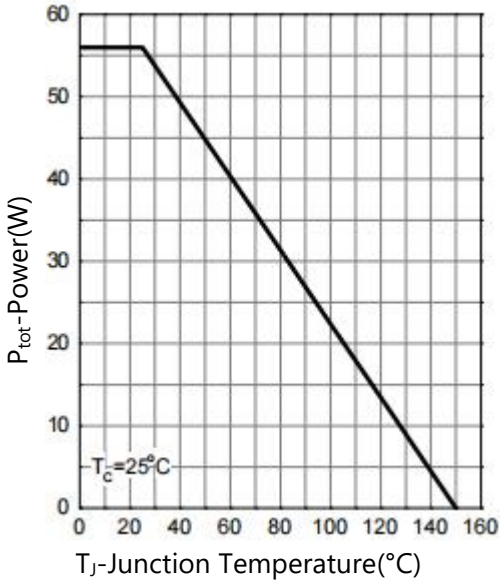


Figure 2. Current Capability

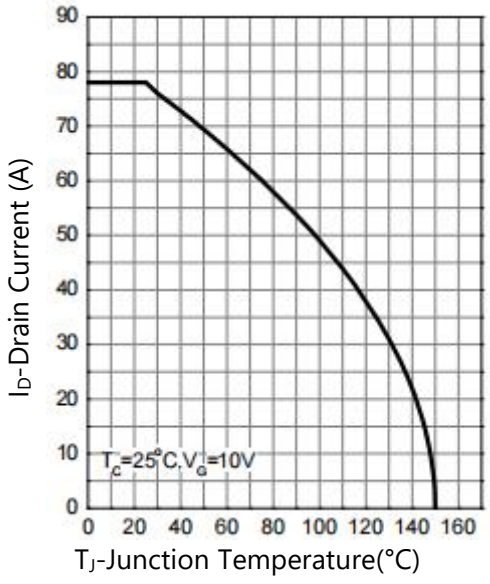


Figure 3. Safe Operation Area

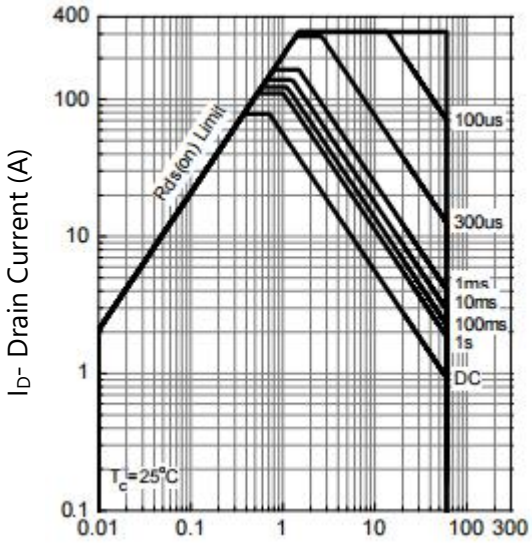
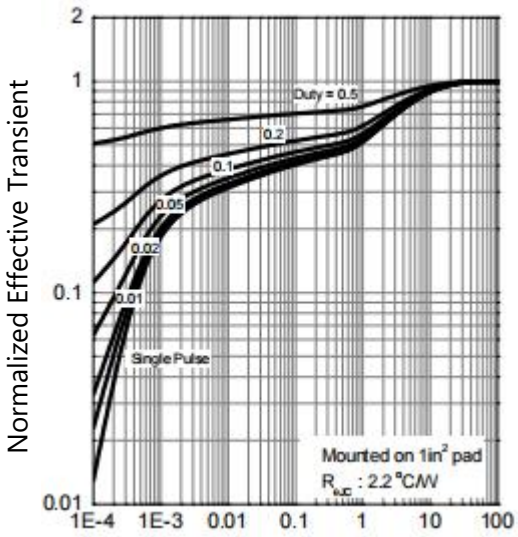


Figure 4. Transient Thermal Impedance



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Figure 5. Output Characteristics

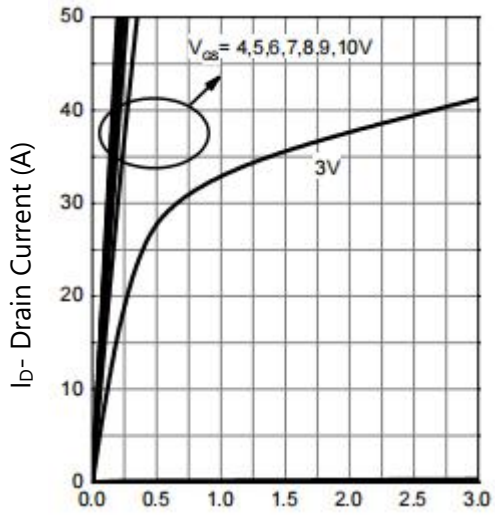


Figure 6. Drain-Source On Resistance

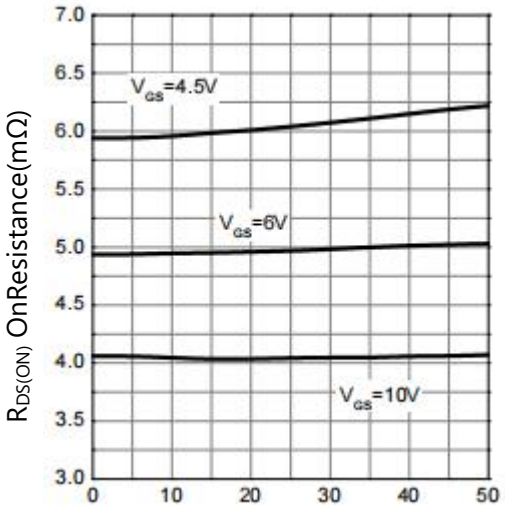


Figure 7. Transfer Characteristics

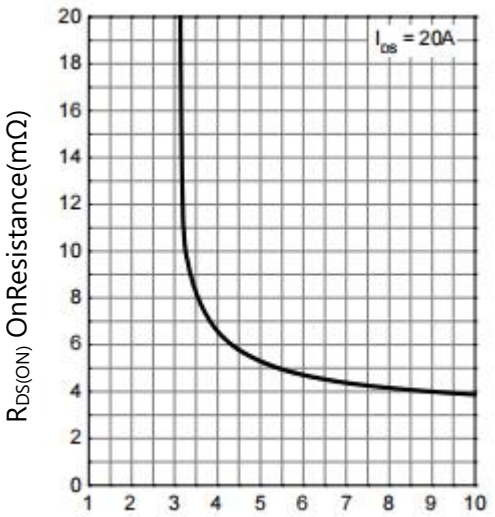
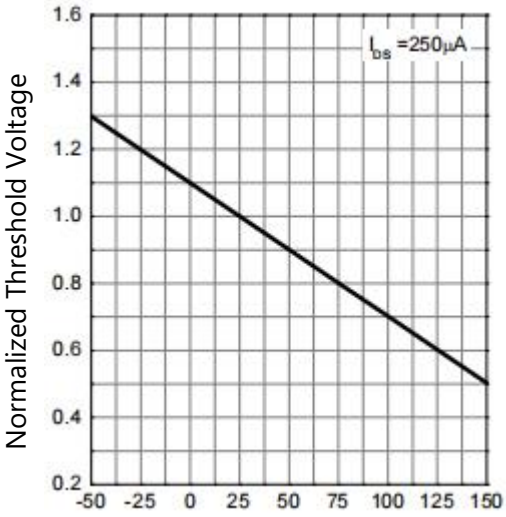


Figure 8. Gate Threshold Voltage



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

Figure 9. Drain-SourceOn Resistance

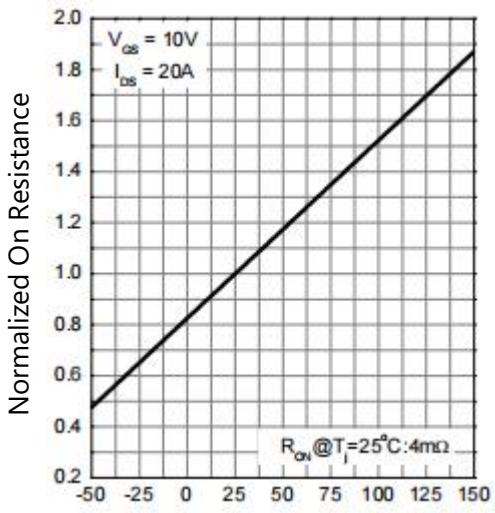


Figure 10. Source-Drain Diode Forward

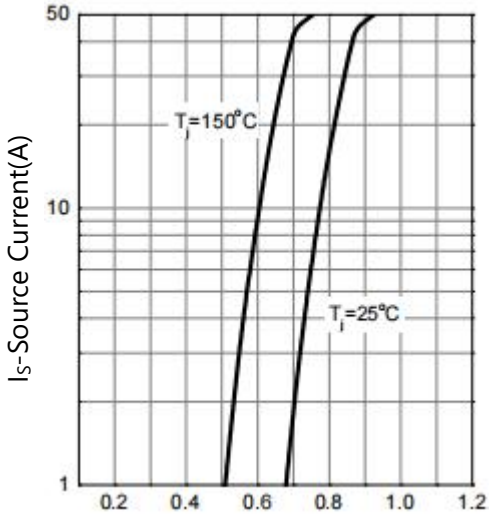


Figure 11. Capacitance

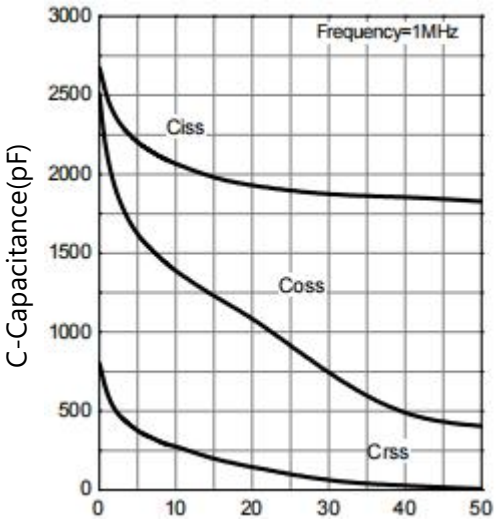
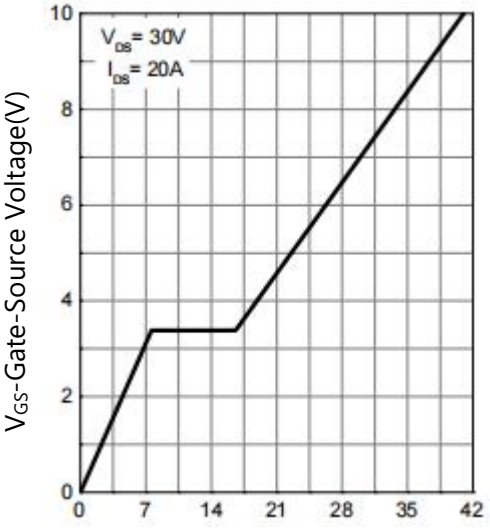
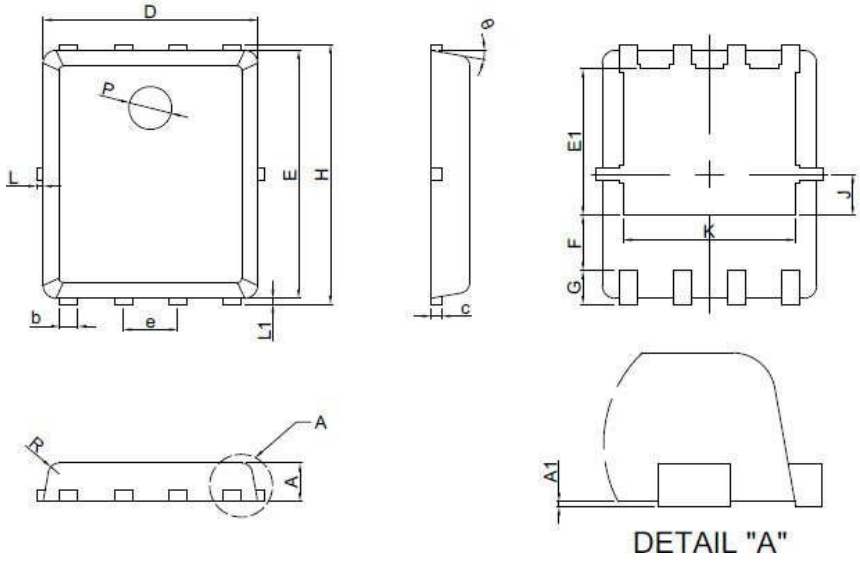


Figure 12. Gate Charge



PACKAGE INFORMATION

PDFN5X6-8L



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	0.80	1.00
A1	0.00	0.05
b	0.35	0.49
c	0.254REF	
D	4.90	5.10
F	1.40REF	
E	5.70	5.90
e	1.27BSC	
H	5.95	6.20
L1	0.10	0.18
G	0.60REF	
K	4.00REF	
L	-	0.15
J	0.95BSC	
P	1.00REF	
E1	3.40REF	
θ	6°	14°
R	0.25REF	